Interactions between Working Memory and Long-term Memory

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Interactions between WM and LTM

- Paul J. Reber, Northwestern Univ. – Intro, fMRI
- Charan Ranganath, UC Davis

 fMRI
- Wendy Suzuki, New York Univ. – Electrophysiology
- Randy O'Reilly, Univ. of Colorado

 Computational modeling
- Art Shimamura, Berkeley

 Discussant

Memory Systems

- WM: temporary maintenance
 - activation based, rapid updating, flexible
 - DLPFC networks
- LTM: long-term storage
 synaptic-change, slow consolidation,
 - episodic – MTL networks

An old idea about WM/LTM interactions Sensory Registers Strs: Rehearsal Manipulation Short term store to Long term store Atkinson & Shiffrin (1968); Broadbent (1958) Cognitive Revolution Postulating processes and representations in the mind/brain STS/LTS gave way to single LTM store and separate WM system

WM as the gateway to LTM

- WM rehearsal time does not predict LTM storage on recall tests
 - <u>Elaborative</u> rehearsal is more effective than <u>maintenance</u> rehearsal
 - e.g., Craik & Watkins, 1973
 - May predict better performance on recognition tests (Glenberg et al., 1977)
- Patients with WM deficits can show normal LTM acquisition (Warrington & Shallice, 1969)

LTM supports WM

- Knowledge from long-term memory structures extends WM span
 - "Long-term WM" (Ericsson & Kintsch, 1995)– Chunking:
 - Ericsson, Chase & Faloon (1980) 79-digit span
 Chase & Simon (1973) chess expertise
- LTM may allow recovery from WM disruption
- Intuitively: recovering from distraction
 - Amnesic patients exhibit WM deficits at long delays (Buffalo et al., 1998) or with supra-span lists (Drachman & Arbit, 1966).

Neuroimaging

- LTM and WM are frequently imaged independently
- Cabeza & Nyberg (2000): >50 WM, >100 LTM • Both types of memory are associated with
- widespread activity in a variety of PFC areas
- In addition to "signature" areas: DLPFC, MTL
- Ranganath & D'Esposito (2001)
 - MTL activity during WM delay
 - What process is this?

Understanding MTL activity during WM maintenance

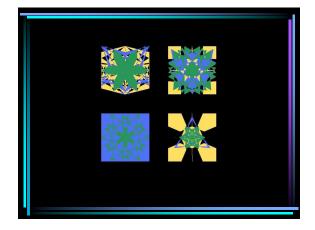
- WM as gateway to LTM or is LTM supporting WM performance?
- Examine MTL activity during WM and subsequent LTM
 - If this activity reflects <u>encoding</u>, then it should be associated with subsequent recognition (LTM success)
 - If this activity reflects <u>retrieval</u>, then effects should be observed on WM performance

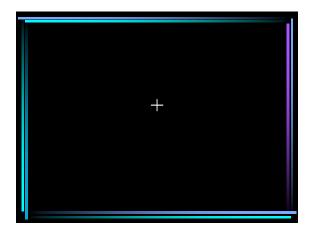
LTM processes during WM

- Participants perform WM task in scanner
- Check for recognition of stimulus arrays after WM testing is complete
- Is there spontaneous LTM encoding during WM? Successful WM should predict successful LTM
- If WM acts as a gateway to LTM • fMRI during WM
 - Find dM effects
 - Activity predicting later recognition
 Find dWM effects
 - · Activity predicting successful WM response

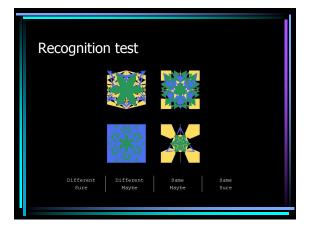
Experimental Design

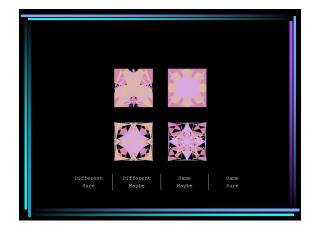
- WM paradigm with 4-element arrays
 - 3 sec to encode array
 - Complex, nonverbal polygon stimuli
 - Difficult to encode
- 5 second maintenance period Probe & Response
 - Was this probe in the original array? - Yes/no with confidence
- Post-scan Recognition of stimuli
- Did you see this array during scanning?
- Yes/no with high/low confidence

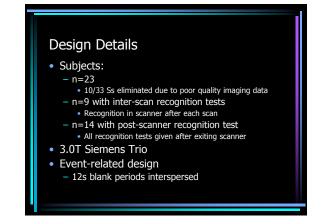


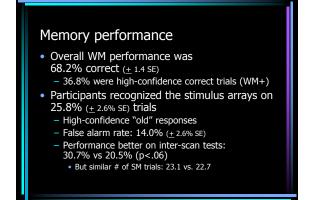


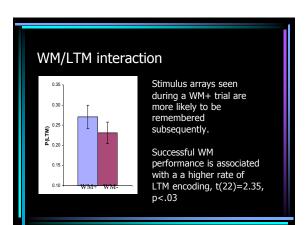


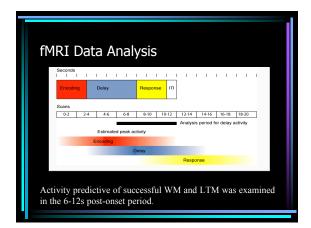


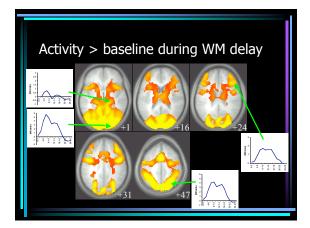


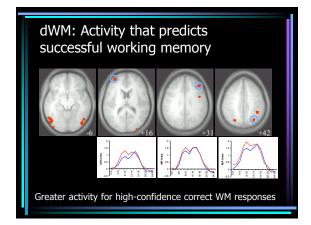


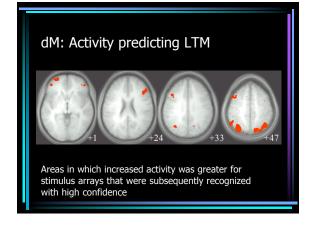


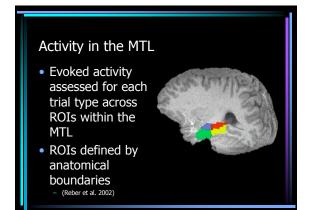


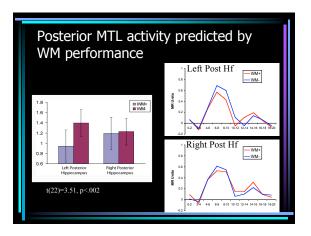


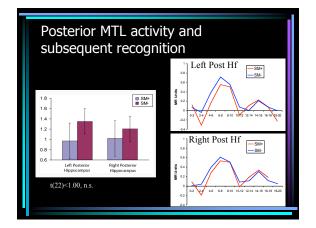


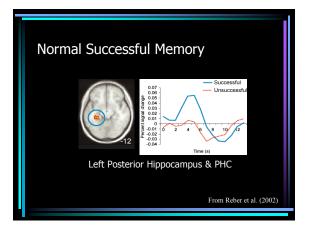






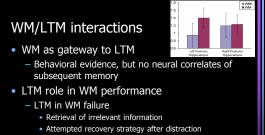






Summary

- MTL activity during WM found to be related to incorrect WM responses
 - MTL activity does not appear to be associated with LTM encoding in this study
- dWM activity is consistent with previous WM studies
 - DLPFC/Parietal activity
- dM activity is fairly consistent with previous encoding studies
 - LPFC, posterior parietal
 - But no MTL differences



- Four complex stimuli is a high WM load Under high-load conditions, retrieval of LTM information during the delay may play an important role

Project Collaborators

- Antonio Gisbert (graduate student, NU)
- Mike Levitt (research assistant, NU)
- NU Cognitive Brain Mapping Group
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