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3-D CHIPS: Integrated circuits and computer chips made from silicon have been at the core of the electronics business for decades, but transistors and other semiconductor devices may one day migrate to more exotic locales.

Materials researchers at the University of Illinois at Urbana-Champaign have devised techniques for integrating different semiconductors in two and three dimensions on new materials, including flexible plastic sheets.

"Important new types of electronic systems will rely on the ability to mix and match wide- ranging classes of devices in three-dimensional configurations on unusual substrates," said John Rogers, an engineering professor. "Circuits enabled by such approaches will open up interesting application possibilities that lie beyond the scope of existing singlematerial, wafer-scale electronics."

The nanoscale printing process developed by the researchers "shows that it is possible to liberate high-performance electronic devices from semiconductor wafers and integrate them onto surfaces that better serve important applications," said Ralph Nuzzo, a chemistry professor who worked with Rogers to co-author a description of their techniques for the journal Science.



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